



Atty. Dkt. No. 035905-0134

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kedar PATEL et al.  
Title: HIGH DENSITY 3D RAIL STACK ARRAYS  
AND METHOD OF MAKING  
Appl. No.: 10/779,760  
Filing Date: 02/18/2004  
Examiner: Thinh Nguyen  
Art Unit: 2818

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop AMENDMENT  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In response to the Restriction Requirement dated August 25, 2004, applicants hereby elect, with traverse, claims of Group I, claims 33-38 and 62-64, drawn to a semiconductor device, classified in class 257, subclass 67.

The traverse is on the ground that Applicants request rejoinder of method claims of equal or narrower scope to any allowed device claim, as provided in MPEP § 821.04.

Applicants, of course, reserve the right to file a divisional application covering the subject matter of the non-elected claims. Receipt of the initial Office Action on the merits is awaited.

Respectfully submitted,

Date 9/10/04

By Leon Radomsky

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